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U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

7R4. Q.A

2

APPLICATION NO.
09/927444

CONT/PRIOR

CLASS

SUBCLASS

ART UNIT

EXAMINER

APPLICANTS

374

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2125

Dynamic control of wafer processing paths in semiconductor manufacturing processes

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL						CROSS REFERENCE(S)						
CLASS			SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				
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